UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,502,463 B2

Page 1 of 2

APPLICATION NO.: 09/892310

DATED

: March 10, 2009

INVENTOR(S)

: Oi et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 34, "Calif" should be deleted and replaced with -- CA--.

Column 10, Line 20, "P(P(bitsequence))" should be deleted and replaced with --P(P-1(bitsequence))--.

Column 14, Line 47, "NI to M" should be deleted and replaced with --N + 1 to M--.

Column 14, Line 48, "circuit associated" should be deleted and replaced with --circuit layout associated--.

Column 14, Line 51 "circuit associated" should be deleted and replaced with --circuit layout associated ---.

Column 15, Line 18, "circuit associated" should be deleted and replaced with --circuit layout associated --.

Column 15, Line 21, "circuit associated" should be deleted and replaced with --circuit layout associated--.

Column 15, Line 24, "circuit associated" should be deleted and replaced with --circuit layout associated ---.

Column 15, Line 27, "circuit associated" should be deleted and replaced with --circuit layout associated--.

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Page 2 of 2

DATED

APPLICATION NO.: 09/892310 : March 10, 2009

INVENTOR(S)

: Qi et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 16, Line 43, "Nl to M" should be deleted and replaced with --N + 1 to M--.

Signed and Sealed this

Nineteenth Day of May, 2009

John Wall

JOHN DOLL Acting Director of the United States Patent and Trademark Office